

IIIT Hyderabad Invites Applications for the Position of Ph.D./JRF/MS/Research-Associate (RA) at IC-WiBES Research Lab at the Center for VLSI Design and Embedded Systems Technology (CVESST), IIIT Hyderabad

Advertisement No: IIITH/RnD Recruitments/Advt No:07/2023

Application Start Date: 20 November 2023

End Date: 15 January 2024

The International Institute of Information Technology Hyderabad is a world-class institution, where the passion for learning, teaching, research, problem solving, and making a difference is celebrated. With emphasis on research, the institute has large research centers with PhD & MS by Research students working on wide ranging topics. The institute offers B.Tech, MS by Research, Dual Degree (B.Tech + MS), M.Tech and PhD programs. IIIT-H provides a flexible environment that facilitates active industry collaborations for conducting research.

Principal Investigator: Prof. Abhishek Srivastava

Webpage: <https://fac-webpages.iiit.ac.in/abhishek/>

Project-1: Development of Silicon Proven IP Cores, Transceiver IC, and System Prototype for mmWave Radar Sensing in Healthcare and Security Applications" Funded by Ministry of Electronics and Information Technology (MeitY) under the Chips to Startup (C2S) program.

This project aims to design & tape-out ICs of energy efficient mmWave radar transceivers (24/77 GHz) and develop radar-based sensing systems for healthcare and security applications.

Position with Academic qualifications and remuneration:

- 1.JRF/Ph.D.: Minimum qualification: B. TECH (ECE/EE/Electronics related), MTech / MS preferred.
salary: 37000/- per month + HRA (if applicable).
- 2.MS/RA: Minimum qualification: B. TECH (ECE/EE/Electronics related).
salary: 15000/- per month + HRA (if applicable).

Project-2: "Gold Aid - A safety device for plant workers to enable fastest medical Aid in the Golden hour" Funded by Department of Science and Technology (DST) under the Technology Development Program (TDP).

In this project, an IoT and AI enabled worker safety system is being developed for large factories such as thermal power plants, oil and natural gas plants, coal mines and steel factories.

Position with Academic qualifications and remuneration:

- 1.JRF/Ph.D.: Minimum qualification: B. TECH (ECE/EE/Electronics related), MTech/MS preferred, experience in embedded system design is preferred.
salary: 37000/- per month + HRA (if applicable)
- 2.Project staff: Minimum qualification: B. TECH (ECE/EE/Electronics related), experience in embedded system design is preferred.
salary: 20000/- per month + HRA (if applicable)

Type of Role: Durations of the contract period is from 1 year to 4 years.

To Apply: As per the project you are applying to the positions in C2S Project or DST-TDP project. Interested candidates can email CV to hiring.projectstaff@iiit.ac.in, CC'ing: abhishek.srivastava@iiit.ac.in with subject line by mentioning the project name and position.

Shortlisted Criteria: Candidates will be shortlisted based on their academic qualifications and experience and will be invited for interviews after December 15, 2023.